

Abstract

A liquid crystal display element includes array and counter substrates provided opposite to each other.

5 The array substrate is covered with a color filter layer. Pixel electrodes are provided in a matrix form on the color filter layer. The surface of the counter substrate is provided with a common electrode. Alignment films are coated on the pixel and common electrodes. A gap defined
10 between the array and counter substrates are filled with a liquid crystal material to form a liquid crystal layer. The alignment films are processed to have surface energy within the range from 51 to 60 dyn/cm. Such surface energy substantially prevents an image-sticking phenomenon of the
15 liquid crystal display element caused by impurities dissolved into a liquid crystal layer and white or black turbid spots caused by hydrolysis of the alignment films by moisture in the liquid crystal layer.